

10/000347  
12/04/01



U.S. UTILITY Patent Application

PATENT NUMBER and  
ISSUE DATE

APPL NUM 10000347	FILING DATE 12/04/2001	CLASS 327	SUBCLASS 047.300	GAU 2816	EXAMINER Patel G.
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\*\*APPLICANTS: Kudo Mamoru; Nakamura Shinobu;

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2653

2655

\*\*CONTINUING DATA VERIFIED:

\*\* FOREIGN APPLICATIONS VERIFIED:

JAPAN P2000-374695 12/05/2000

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☐ yes ☐ no

35 USC 119 conditions met ☐ yes ☐ no

Verified and Acknowledged Examiners's initials

ATTORNEY DOCKET NO

SON-2274

TITLE : Phase-locked loop circuit for reproducing A channel lock

U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		<b>CLAIMS ALLOWED</b>	
		Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		<b>DRAWING</b>	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		Application Examiner	
		<b>PREPARED FOR ISSUE</b>	
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